

DEPLOYING
VERIFICATION
METHODS FOR THE
SECURITY OF
CYBER-PHYSICAL
SYSTEMS INTO
FAST, FLEXIBLE
SYSTEM
ENGINEERING
PRACTICES



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VeriDevOps



New tools to increase the
security of DevOps

OUR VISION

DevOps helps increase an organisation's ability to deliver applications and services at high velocity. It aims to shorten the systems development life cycle and provide continuous delivery with high-quality software. Current systems development practices are increasingly based on off-the-shelf and legacy components, which make such systems prone to security vulnerabilities.

VeriDevOps is an european project managed by Mälardalen University and led too by 5 other partners: ABB, Åbo Akademi, SOFTEAM, Montimage, IKERLAN and Fagor Arrasate S. Coop.. Four countries got united to work in #VeriDevOps: Spain, Finland, Sweden and France.

OUR OBJECTIVE

VeriDevOps brings together fast security verification through formal modelling and verification as well as test generation, selection, execution and analysis capabilities to enable companies to deliver quality systems with confidence in a fast-paced DevOps environment. VeriDevOps brings fast and cost-effective security formal verification and test automation thus significantly improving the DevOps processes. Overall, VeriDevOps is using the results of formal verification of security requirements for test and monitor generation to be used to enhance the feedback mechanisms during development and operation phases.

OUR MISSION

To create a methodology and a set of integrated mechanisms that significantly improve automation in VeriDevOps to protect systems at operations time and preventing security issues at development time by:

1. Formalizing security requirements,
2. Generating trace monitors
3. Locating root causes of vulnerabilities,
4. Identifying security flaws in code and designs.

This will significantly improve productivity and enable the continuous integration/delivery of trustworthy systems.